



Doc. Number :

- ☐ Tentative Specification
☐ Preliminary Specification
☒ Approval Specification

MODEL NO.: V185BG2
SUFFIX: LE1

Customer: Common

APPROVED BY

SIGNATURE

Name / Title

Note

Please return 1 copy for your confirmation with your signature and comments.

Approved By	Checked By	Prepared By
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**REVISION HISTORY**

Version	Date	Page	Description
2.0	Aug.16, 2011	All	Spec Ver.2.0 was first issued.



1. GENERAL DESCRIPTION

1.1 OVERVIEW

V185BG2-LE1 is a 18.5" TFT Liquid Crystal Display module with WLED Backlight unit and 30 pins 1ch-LVDS interface. This module supports 1366 x 768 HD mode and can display up to 16.7M colors. The converter module for Backlight is not built in.

1.2 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Screen Size	18.51" real diagonal	-	-
Driver Element	a-si TFT active matrix	-	-
Pixel Number	1366 x R.G.B. x 768	pixel	-
Pixel Pitch	0.3 (H) x 0.3 (V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	16.7M	color	-
Transmissive Mode	Normally white	-	-
Surface Treatment	AG type, 3H hard coating, Haze 25%	-	-
Luminance, White	225 (Typ.) (Target)	Cd/m2	-
Color Gamut	72% of NTSC(Typ.)	-	-
TCO	TCO 5.0 compliance	-	-
Power Consumption	Total 8.95W (Typ.) @ cell 2.5 W (Typ.), BL 6.45W (Typ.)	-	(1)

Note (1) The specified power consumption : Total= cell (reference 4.3.1)+BL (reference 4.3.3)

2. MECHANICAL SPECIFICATIONS

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	429.87	430.37	430.87	mm	(1)
	Vertical (V)	254.1	254.6	255.1	mm	
	Thickness (T)	-	10.5	11	mm	
Bezel Area	Horizontal	412.9	413.4	413.9	mm	-
	Vertical	239.5	234	234.5	mm	-
Active Area	Horizontal	-	409.8	-	mm	-
	Vertical	-	230.4	-	mm	-
Weight		-	1600	1800	g	-

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

3. ABSOLUTE MAXIMUM RATINGS

3.1 ABSOLUTE RATINGS OF ENVIRONMENT

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Storage Temperature	TST	-20	+60	°C	(1)
Operating Ambient Temperature	TOP	0	50	°C	(1), (2)
Shock (Non-Operating)	SNOP	-	50	G	(3), (5)
Vibration (Non-Operating)	VNOP	-	1.0	G	(4), (5)

Note (1) Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. ($T_a \leq 40\text{ }^{\circ}\text{C}$).

(b) Wet-bulb temperature should be 39 °C Max. ($T_a > 40\text{ }^{\circ}\text{C}$).

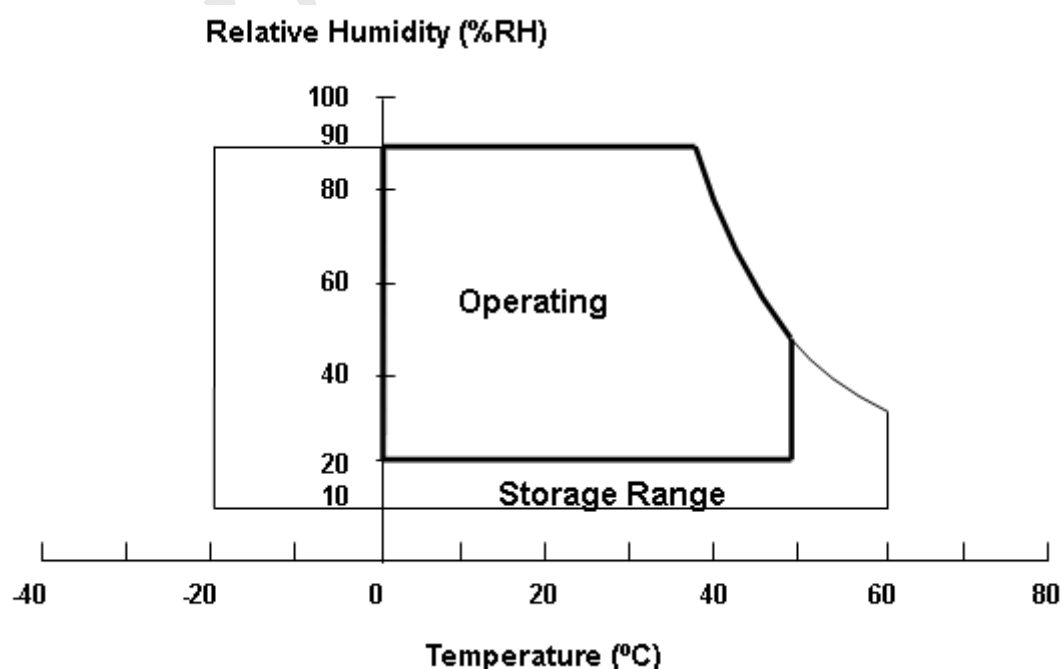
(c) No condensation.

Note (2) The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 65 °C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 65 °C. The range of operating temperature may degrade in case of improper thermal management in final product design.

Note (3) 11 ms, half sine wave, 1 time for $\pm X$, $\pm Y$, $\pm Z$.

Note (4) 10 ~ 200 Hz, 10 min, 1 time each X, Y, Z.

Note (5) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.



3.2 ELECTRICAL ABSOLUTE RATINGS

3.2.1 TFT LCD MODULE

Item	Symbol	Value		Unit	Note
		Min.	Max.		
Power Supply Voltage	VCCS	-0.3	6.0	V	(1)
Logic Input Voltage	V _{IN}	-0.3	3.6	V	

3.2.2 BACKLIGHT UNIT

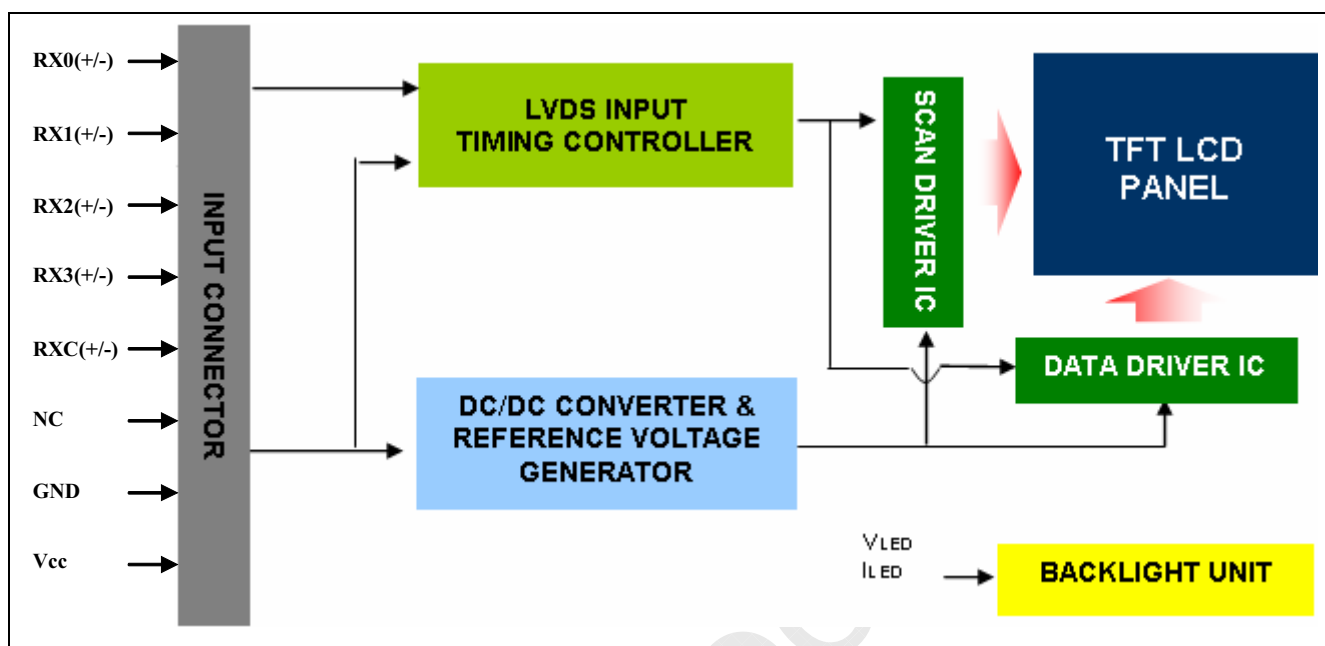
Item	Symbol	Value			Unit	Note
		Min.	Typ	Max.		
LED Forward Current Per Input Pin	I _F	-	65	69	mA	(1), (2) Duty=100%
LED Pulse Forward Current Per Input Pin	I _P	-	-	150	mA	(1), (2) Pulse Width ≤ 10msec. and Duty ≤ 30%

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for input pin of LED light bar at Ta=25±2 °C (Refer to 4.3.3 and 4.3.4 for further information).

4. ELECTRICAL SPECIFICATIONS

4.1 FUNCTION BLOCK DIAGRAM



4.2. INTERFACE CONNECTIONS

PIN ASSIGNMENT

Pin	Name	Description
1	NC	Not connection, this pin should be open
2	NC	Not connection, this pin should be open
3	NC	Not connection, this pin should be open
4	GND	Ground
5	RX0-	Negative LVDS differential data input. Channel 0 (odd)
6	RX0+	Positive LVDS differential data input. Channel 0 (odd)
7	GND	Ground
8	RX1-	Negative LVDS differential data input. Channel 1 (odd)
9	RX1+	Positive LVDS differential data input. Channel 1 (odd)
10	GND	Ground
11	RX2-	Negative LVDS differential data input. Channel 2 (odd)
12	RX2+	Positive LVDS differential data input. Channel 2 (odd)
13	GND	Ground
14	RXCLK-	Negative LVDS differential clock input. (odd)
15	RXCLK+	Positive LVDS differential clock input. (odd)
16	GND	Ground
17	RX3-	Negative LVDS differential data input. Channel 3 (odd)
18	RX3+	Positive LVDS differential data input. Channel 3 (odd)
19	GND	Ground
20	NC	For LCD internal use only, Do not connect
21	NC	For LCD internal use only, Do not connect
22	NC	For LCD internal use only, Do not connect
23	GND	Ground
24	GND	Ground
25	GND	Ground
26	Vcc	+5.0V power supply

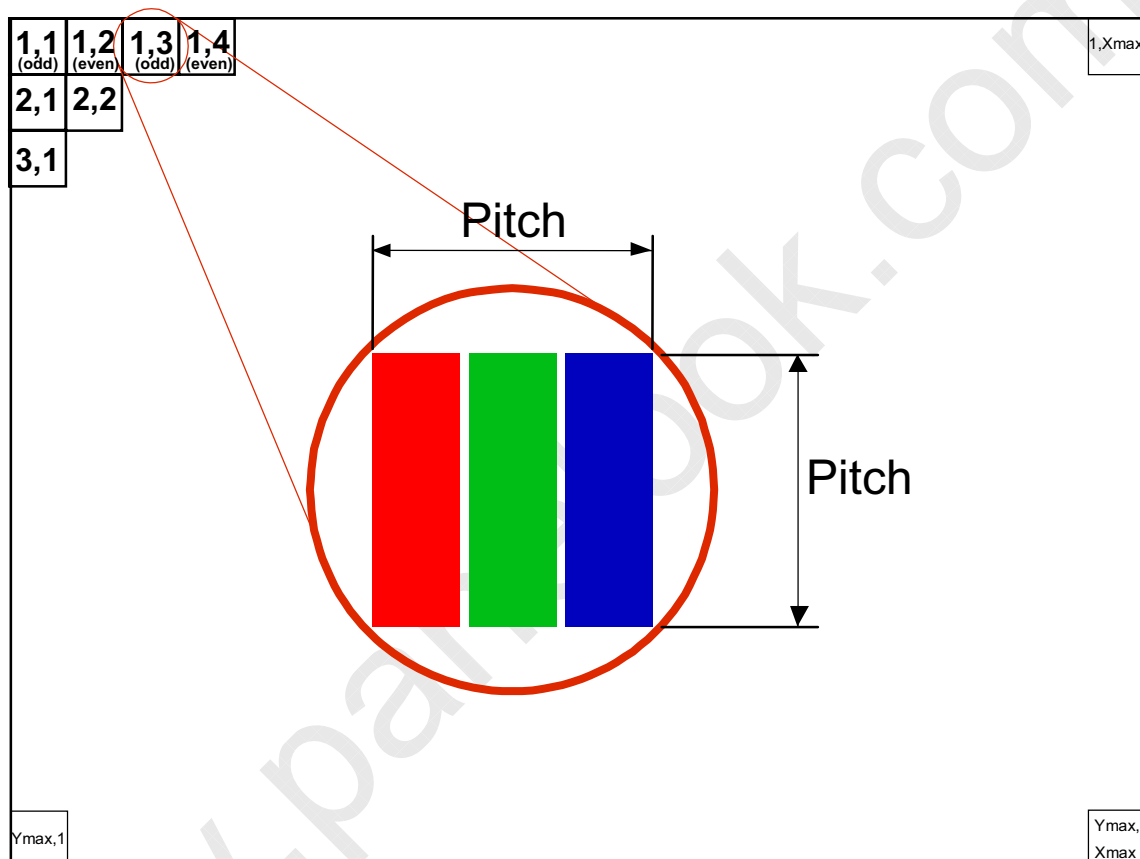
27	Vcc	+5.0V power supply
28	Vcc	+5.0V power supply
29	Vcc	+5.0V power supply
30	Vcc	+5.0V power supply

Note (1) Connector Part No.:

GS23302-0011R-7F (FOXCONN) or equivalent

Note (2) The first pixel is odd.

Note (3) Input signal of even and odd clock should be the same timing.



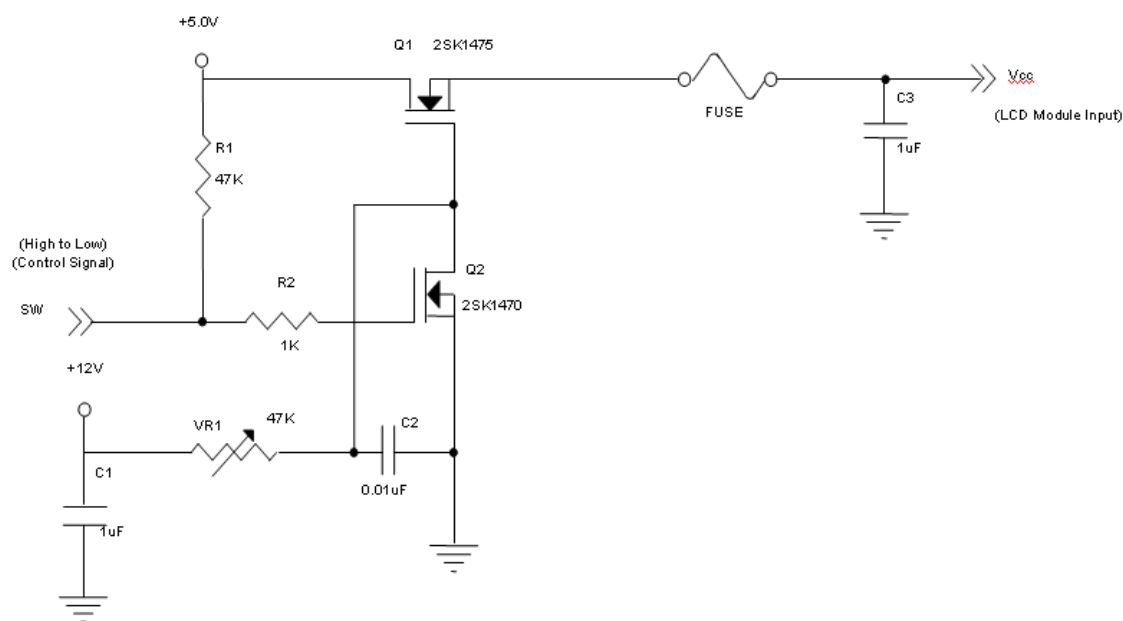
4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD ELETRONICS SPECIFICATION

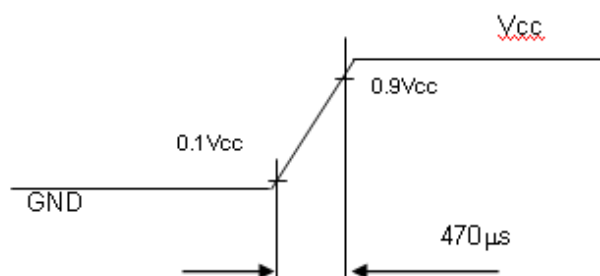
Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	V _{CC}	4.5	5	5.5	V	-
Ripple Voltage	V _{RP}	-	-	300	mV	-
Rush Current	I _{RUSH}	-	1.5	2.0	A	(2)
Power Supply Current	White	-	0.3	0.4	A	(3)a
	Black	-	0.5	0.6	A	(3)b
	Vertical Stripe	-	0.5	0.6	A	(3)c
Power Consumption	PLCD	-	2.5	3.3	Watt	(4)
LVDS differential input voltage	V _{id}	100	-	600	mV	-
LVDS common input voltage	V _{ic}	1.0	1.2	1.4	V	-
Logic High Input Voltage	V _{IH}	-	-	0.1	V	-
Logic Low Input Voltage	V _{IL}	-0.1	-	-	V	-

Note (1) The ambient temperature is $T_a = 25 \pm 2^\circ\text{C}$.

Note (2) Measurement Conditions:



VCC rising time is 470μs



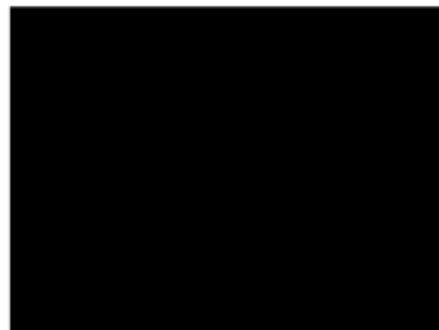
Note (3) The specified power supply current is under the conditions at $V_{cc} = 5.0\text{ V}$, $T_a = 25 \pm 2\text{ }^{\circ}\text{C}$, $F_r = 75\text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



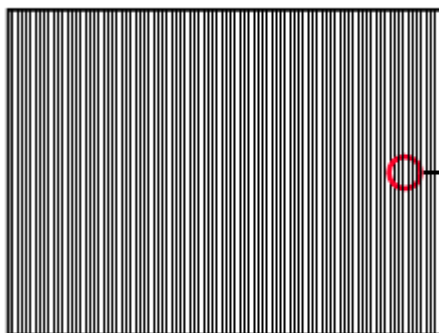
Active Area

b. Black Pattern

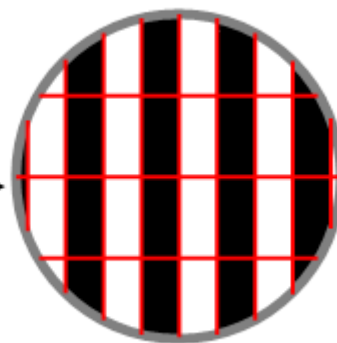


Active Area

c. Vertical Stripe Pattern

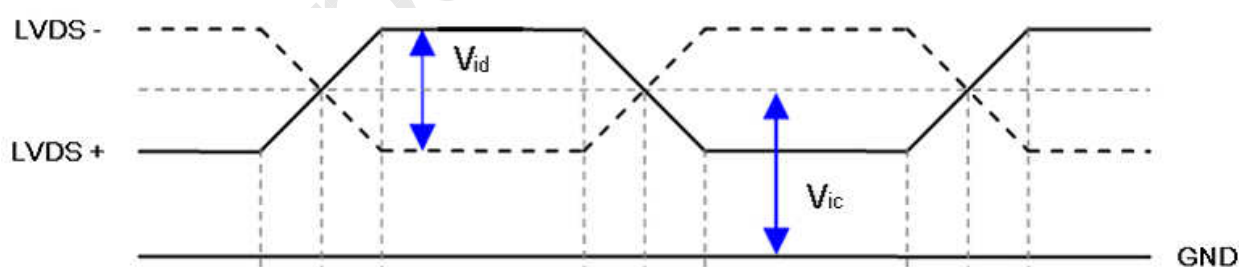


Active Area

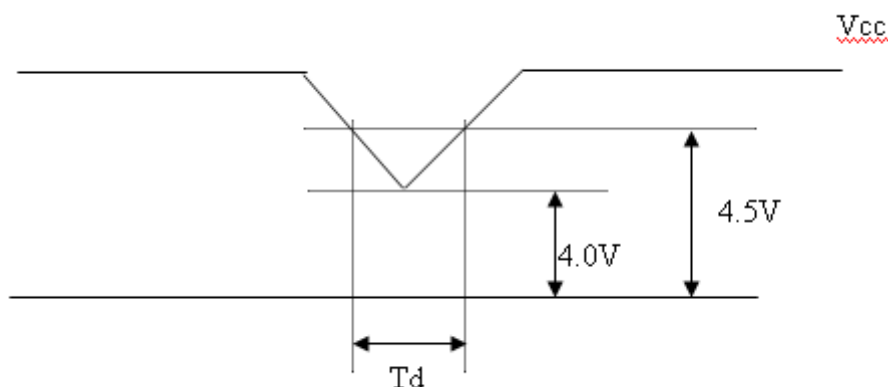


Note (4) The power consumption is specified at the pattern with the maximum current.

Note (5) VID waveform condition



4.3.2 VCC POWER DIP CONDITION



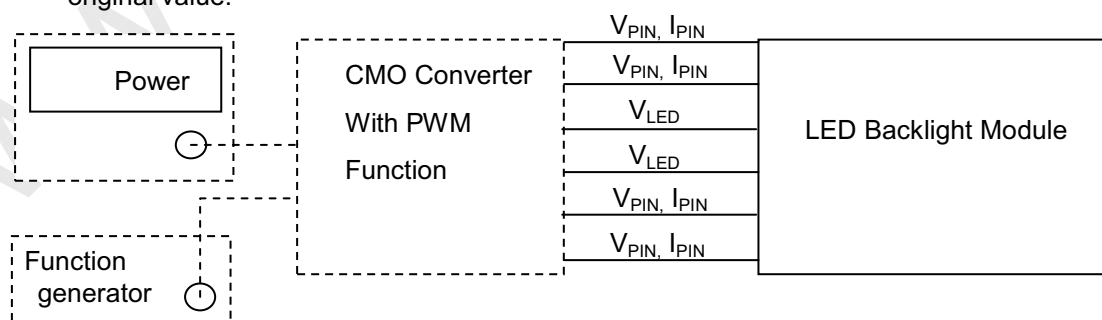
4.3.3 BACKLIGHT UNIT

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
LED Light Bar Input Voltage Per Input Pin	VPIN	-	24.8	27.2	V	(1), Duty=100%, IPIN=65mA
LED Light Bar Current Per Input Pin	IPIN	-	65	69	mA	(1), (2) Duty=100%
LED Life Time	LLED	40000	-	-	Hrs	(3)
Power Consumption	PBL	-	6.45	7.51	W	(1) Duty=100%, IPIN=65mA

Note (1) LED light bar input voltage and current are measured by utilizing a true RMS multimeter as shown below:

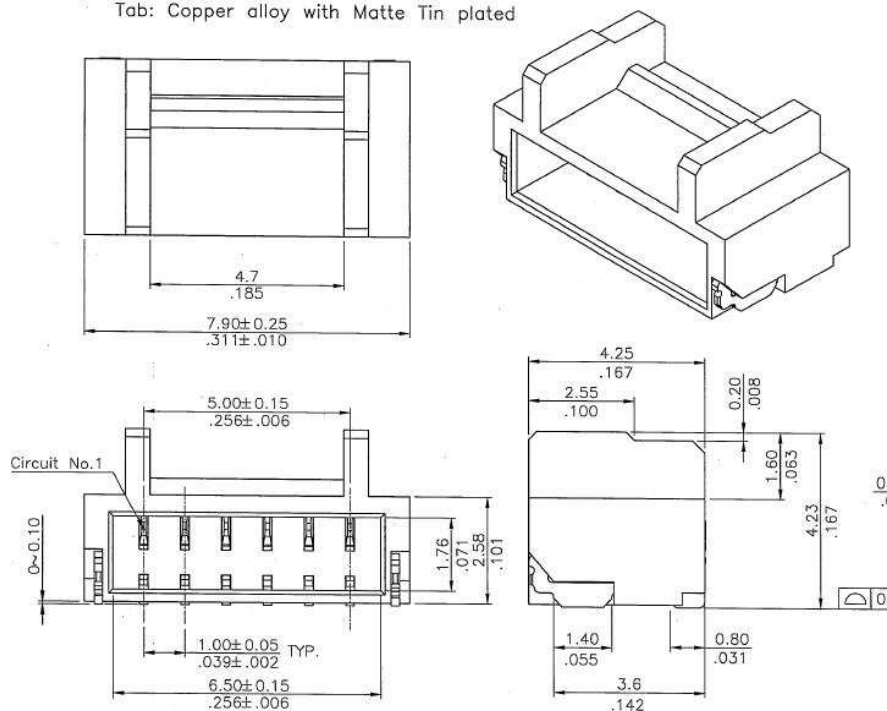
Note (2) $PBL(Typ) = IPIN(Typ) \times VPIN(Typ) \times (4)$, $PBL(Max) = IPIN(Max) \times VPIN(Max) \times (4)$ input pins.

Note (3) The lifetime of LED is defined as the time when LED packages continue to operate under the conditions at $T_a = 25 \pm 2^\circ C$ and $I = (65)mA$ (per chip) until the brightness becomes $\leq 50\%$ of its original value.



4.3.4 LIGHTBAR CONNECTOR PIN ASSIGNMENT

Tab: Copper alloy with Matte Tin plated

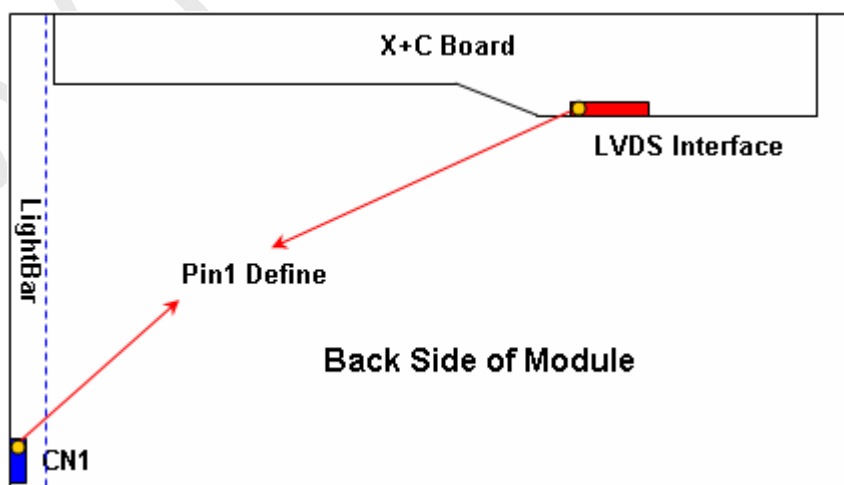


CN1

Pin number	Description
1	Cathode of LED string1
2	Cathode of LED string2
3	VLED
4	VLED
5	Cathode of LED string3
6	Cathode of LED string4

Note(1) Connector : WM13-406-063N or equivalent.

Note(2) User's mating connector part No.: IWF13-00106 or compatible and hook width must be less than 4.5mm.



4.4 LVDS INPUT SIGNAL SPECIFICATIONS

4.4.1 LVDS DATA MAPPING TABLE

LVDS Channel O0	LVDS output	D7	D6	D4	D3	D2	D1	D0
	Data order	OG0	OR5	OR4	OR3	OR2	OR1	OR0
LVDS Channel O1	LVDS output	D18	D15	D14	D13	D12	D9	D8
	Data order	OB1	OB0	OG5	OG4	OG3	OG2	OG1
LVDS Channel O2	LVDS output	D26	D25	D24	D22	D21	D20	D19
	Data order	DE	NA	NA	OB5	OB4	OB3	OB2
LVDS Channel O3	LVDS output	D23	D17	D16	D11	D10	D5	D27
	Data order	NA	OB7	OB6	OG7	OG6	OR7	OR6

4.4.2 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

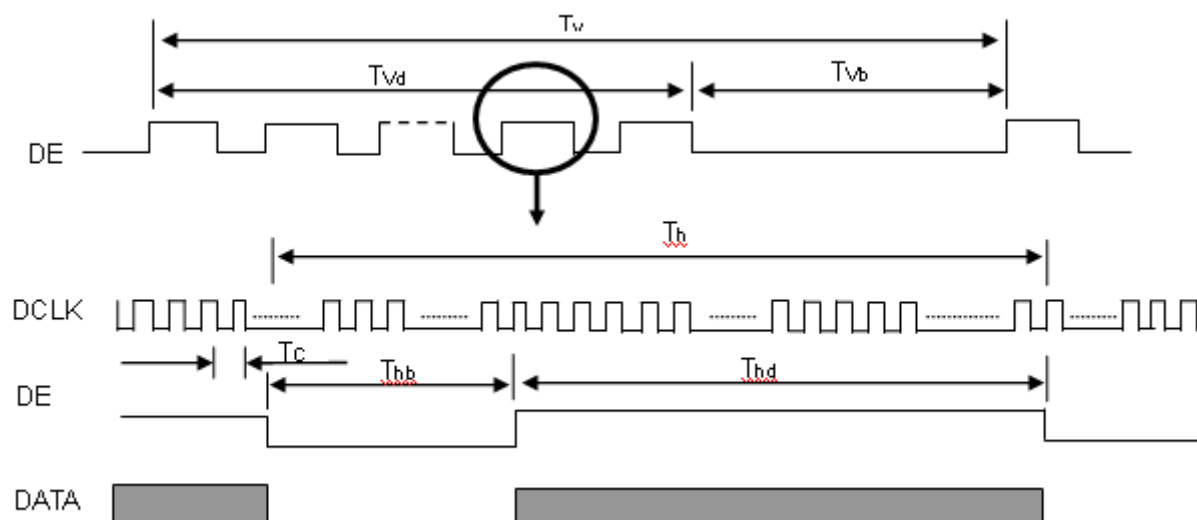
4.5 DISPLAY TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

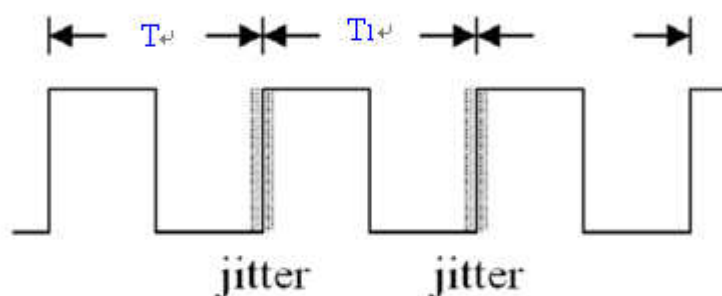
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	Fc	62.9	75.4	95.6	MHz	-
	Period	Tc	10.5	13.2	15.9	ns	
	Input cycle to cycle jitter	T _{rcj}	-0.02*Tc	-	0.02*Tc	ns	(1)
	Input Clock to data skew	TLVCCS	-0.02*Tc	-	0.02*Tc	ns	(2)
	Spread spectrum modulation range	F _{ckin_mod}	0.97*Fc	-	1.03*Fc	MHz	(3)
	Spread spectrum modulation frequency	F _{SSM}	-	-	100	KHz	
Vertical Display Term	Frame Rate	Fr	50	60	76	Hz	Tv=Tvd+Tvb
	Total	Tv	780	806	900	Th	-
	Active Display	Tvd	-	768	-	Th	-
	Blank	Tvb	12	38	132	Th	-
Horizontal Display Term	Total	Th	1500	1560	1600	Tc	Th=Thd+Thb
	Active Display	Thd	-	1366	-	Tc	-
	Blank	Thb	134	194	234	Tc	-

Note: Because this module is operated by DE only mode, Hsync and Vsync input signals are ignored.

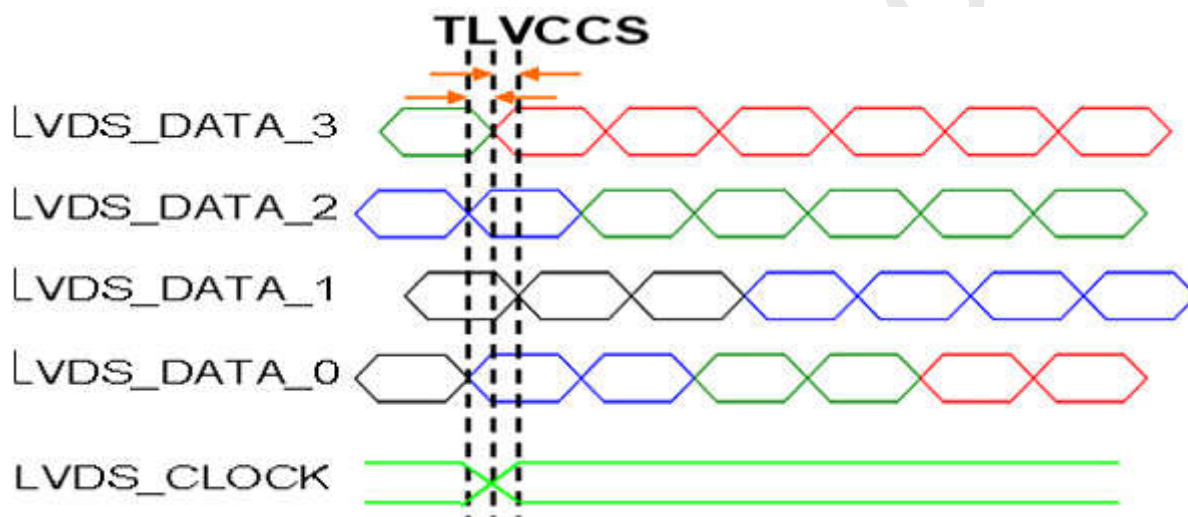
INPUT SIGNAL TIMING DIAGRAM



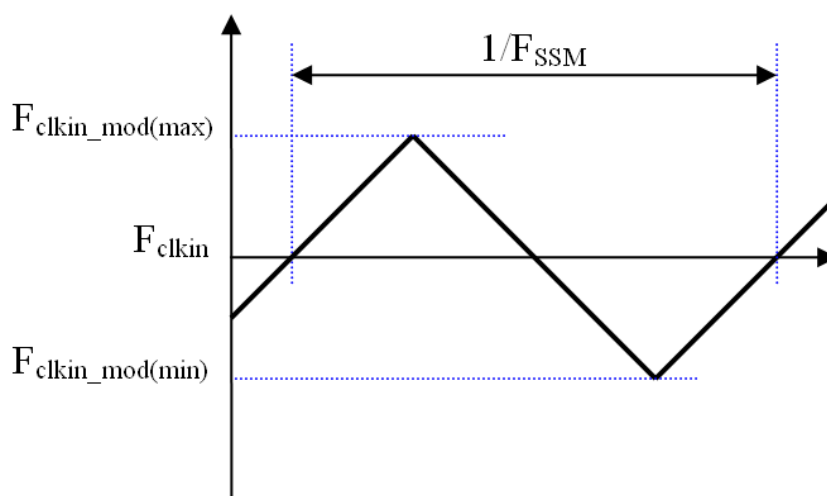
Note (1) The input clock cycle-to-cycle jitter is defined as below figures. $Trcl = |T_1 - T_1|$



Note (2) Input Clock to data skew is defined as below figures.



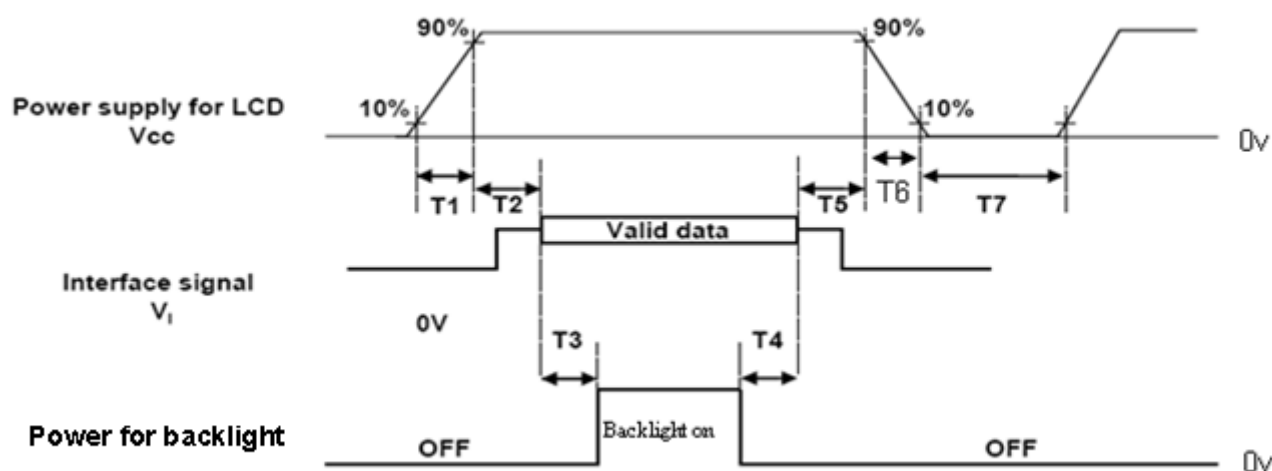
Note (3) The SSCG (Spread spectrum clock generator) is defined as below figures.



Note (4) The DCLK range at last line of V-blanking should be set in 0 to Hdisplay/2.

4.6 POWER ON/OFF SEQUENCE

The power sequence specifications are shown as the following table and diagram.



Timing Specifications:

Parameters	Values			Units
	Min	Typ.	Max	
T1	0.1	-	10	ms
T2	0	30	50	ms
T3	200	250	-	ms
T4	100	250	-	ms
T5	0	20	50	ms
T6	0.1	-	50	ms
T7	1000	-	-	ms

Note (1) The supply voltage of the external system for the module input should be the same as the definition of Vcc.

Note (2) When the backlight turns on before the LCD operation of the LCD turns off, the display may momentarily become abnormal screen.

Note (3) In case of VCC = off level, please keep the level of input signals on the low or keep a high impedance.

Note (4) T4 should be measured after the module has been fully discharged between power off and on period.

Note (5) Interface signal shall not be kept at high impedance when the power is on.

Note (6) CMI won't take any responsibility for the products which are damaged by the customers not following the Power Sequence.

Note (7) There might be slight electronic noise when LCD is turned off (even backlight unit is also off). To avoid this symptom, we suggest "Vcc falling timing" to follow "t6 spec".

5. OPTICAL CHARACTERISTICS

5.1 TEST CONDITIONS

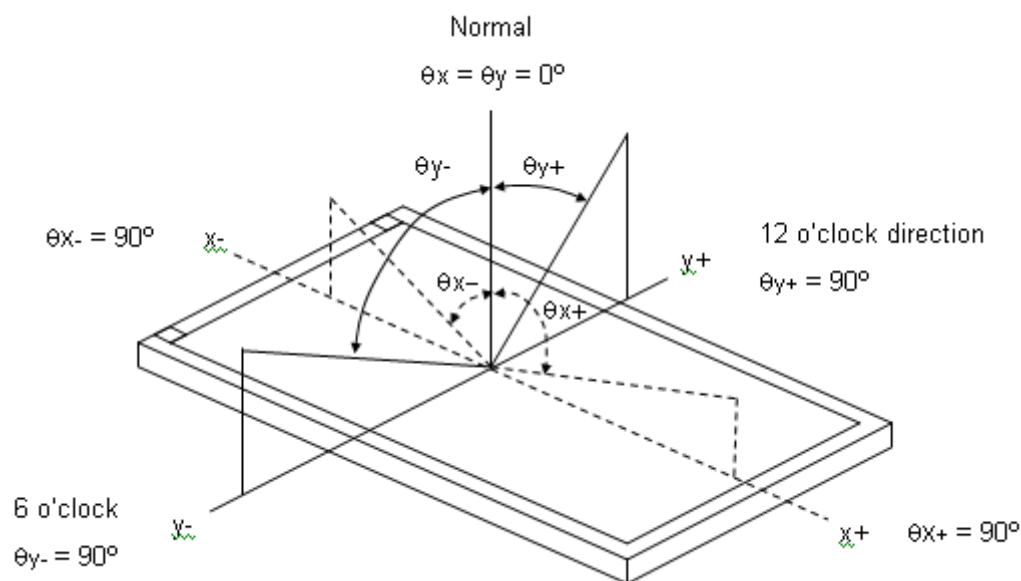
Item	Symbol	Value	Unit
Ambient Temperature	Ta	25±2	°C
Ambient Humidity	Ha	50±10	%RH
Supply Voltage	V _{CC}	5	V
Input Signal	According to typical value in "3. ELECTRICAL CHARACTERISTICS"		
LED Light Bar Input Current Per Input Pin	I _{PIN}	65 ± 1.9	mA _{DC}
PWM Duty Ratio	D	100	%
LED Light Bar Test Converter	TEST01001 T2-A1		

5.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 5.2. The following items should be measured under the test conditions described in 5.1 and stable environment shown in Note (5).

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Color Chromaticity (CIE 1931)	Red	R _x	$\theta_x=0^\circ, \theta_Y=0^\circ$ CS-2000 R=G=B=255 Gray scale	Typ – 0.03	0.626	Typ + 0.03	-	(1), (5)
		R _y			0.332			
	Green	G _x			0.307			
		G _y			0.624			
	Blue	B _x			0.158			
		B _y			0.050			
	White	W _x			0.285			
		W _y			0.293			
	Center Luminance of White (Center of Screen)				L _c			
Contrast Ratio		CR		700	1000	-	-	(2), (5)
Response Time		T _R	$\theta_x=0^\circ, \theta_Y=0^\circ$	-	1.5	2.5	ms	(3)
		T _F		-	3.5	5.5		
White Variation		W	$_x=0, _Y=0$	75	-	-	%	(5), (6)
Viewing Angle	Horizontal	$\theta_{x-} + \theta_{x+}$	CR ≥ 10	150	170	-	Deg.	(1), (5)
	Vertical	$\theta_{y-} + \theta_{y+}$		140	160	-		
Viewing Angle	Horizontal	$\theta_{x-} + \theta_{x+}$	CR ≥ 5	160	178	-	Deg.	(1), (5)
	Vertical	$\theta_{y-} + \theta_{y+}$		150	170	-		

Note (1) Definition of Viewing Angle (θ_x , θ_y):



Note (2) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

$$\text{Contrast Ratio (CR)} = L_{255} / L_0$$

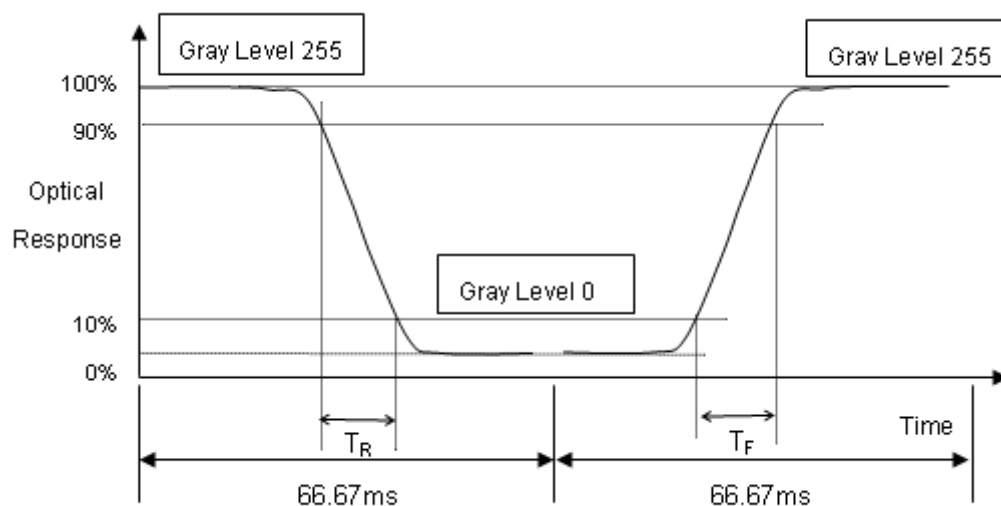
L255: Luminance of gray level 255

L 0: Luminance of gray level 0

$$\text{CR} = \text{CR} (5)$$

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time (T_R , T_F):



Note (4) Definition of Luminance of White (L_c):

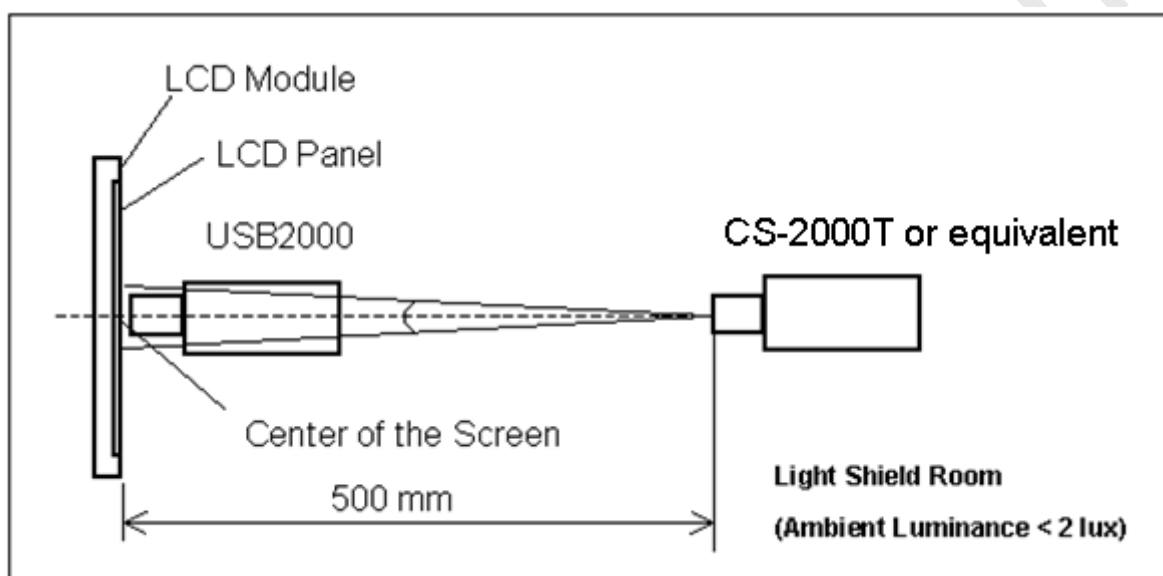
Measure the luminance of gray level 255 at center point

$$L_c = L(5)$$

$L(x)$ is corresponding to the luminance of the point X at Figure in Note (6).

Note (5) Measurement Setup:

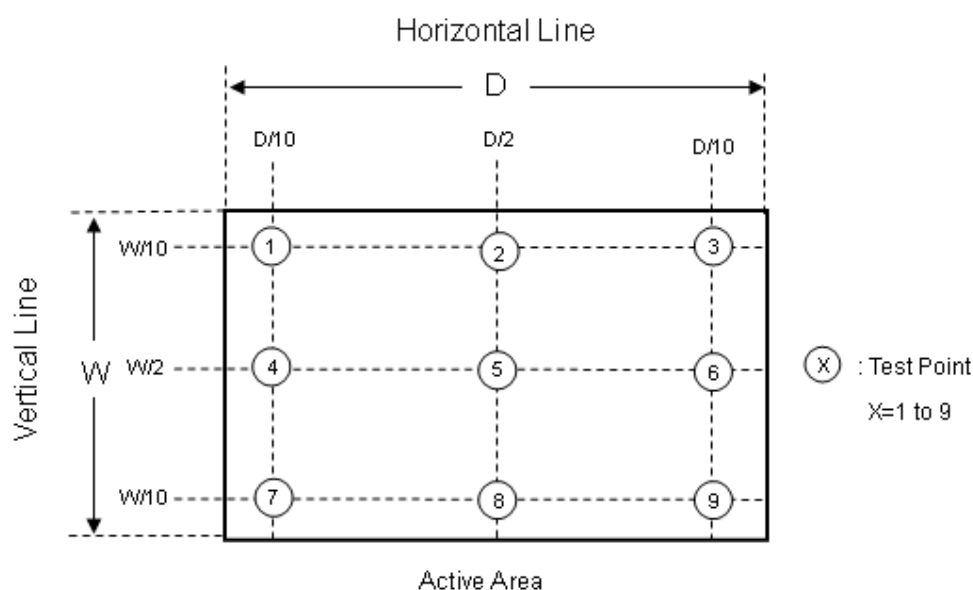
The LCD module should be stabilized at given temperature for 40 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 40 minutes in a windless room.



Note (6) Definition of White Variation (δW):

Measure the luminance of gray level 255 at 9 points

$$\delta W = (\text{Minimum } [L(1) \sim L(9)] / \text{Maximum } [L(1) \sim L(9)]) * 100\%$$



6. PACKING

6.1 PACKING SPECIFICATIONS

- (1) 13 LCD modules / 1 Box
- (2) Box dimensions: 528(L) X 378(W) X 360(H) mm
- (3) Weight: approximately: 22.5 kg (13 modules per box)

6.2 PACKING METHOD

- (1) Carton Packing should have no failure in the following reliability test items.

Test Item	Test Conditions	Note
Vibration	ISTA STANDARD Random, Frequency Range: 1 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	Non Operation
Dropping Test	1 Corner , 3 Edge, 6 Face, 45.7cm	Non Operation

Box Dimensions : 528(L)*378(W)*360(H)mm
13 module .per. 1 box

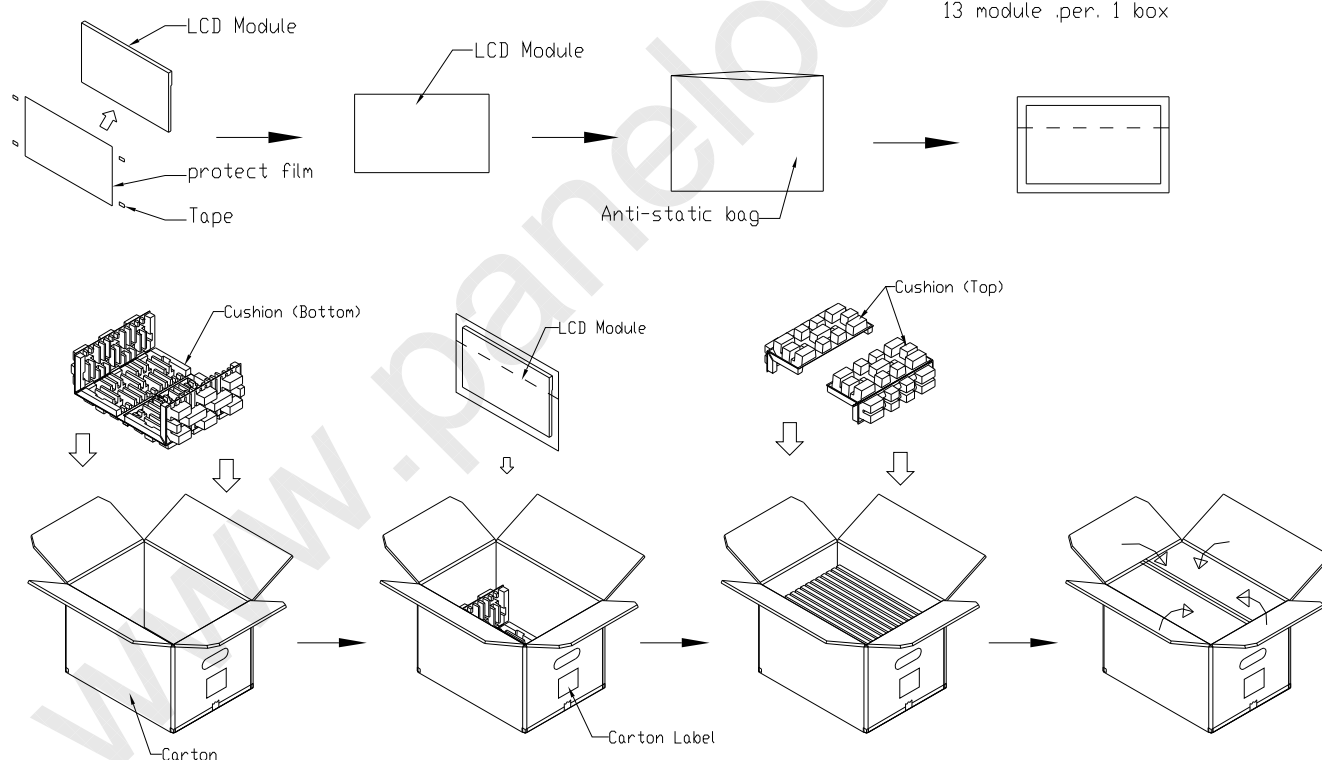
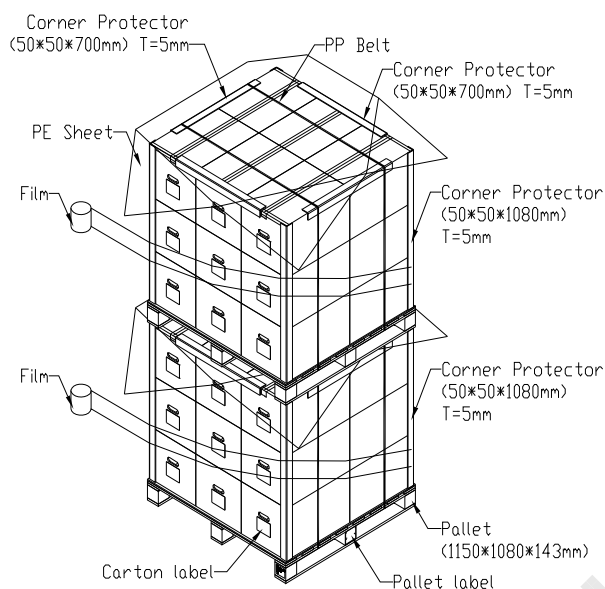


Figure. 6-1 Packing method

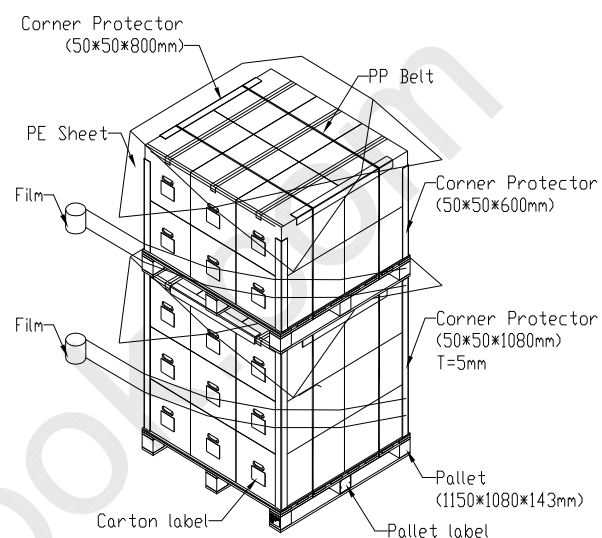


6.3 PALLET

Sea / Land Transportation
(40ft HQ Container)



Sea / Land Transportation
(40ft Container)



Air Transportation

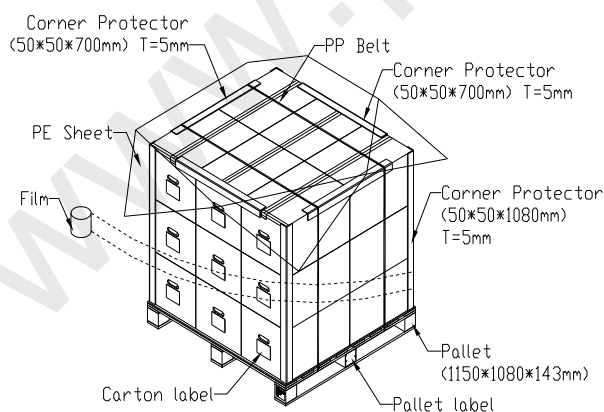
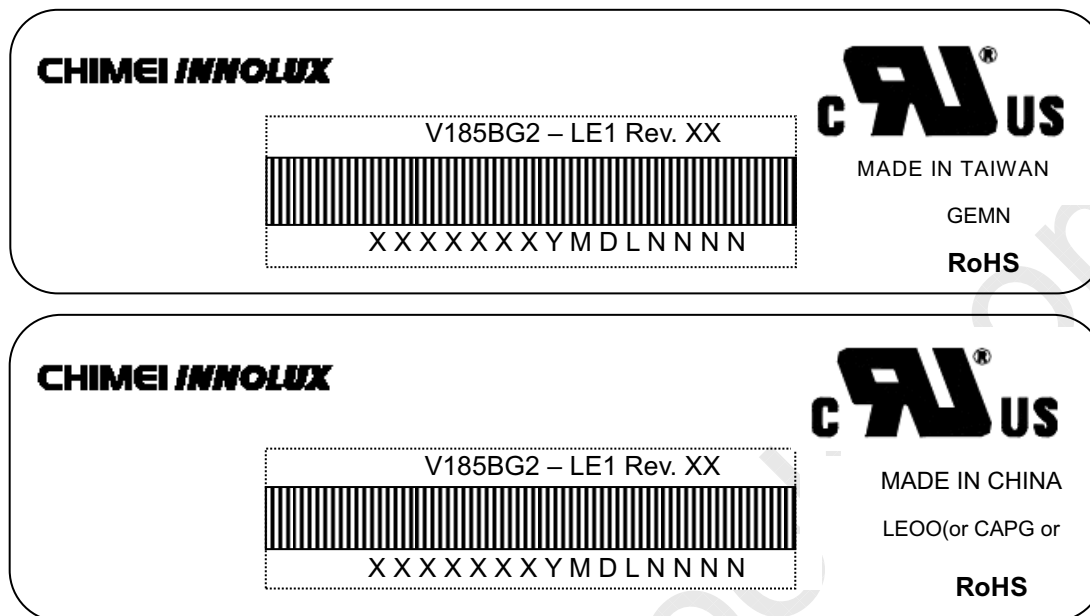


Figure. 6-2 Packing method

7. DEFINITION OF LABELS

7.1 CMO MODULE LABEL

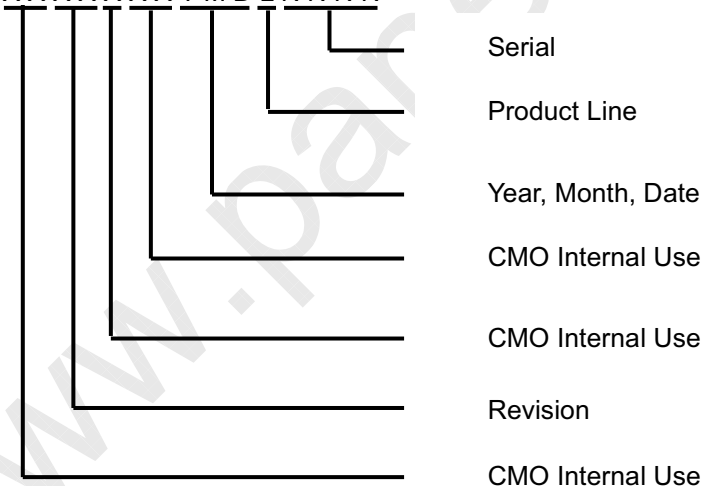
The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



Model Name: V185BG2-LE1

Revision: Rev. XX, for example: A0, A1... B1, B2... or C1, C2...etc.

Serial ID: XXXXXYYMDLNNNN



Serial ID includes the information as below:

Manufactured Date:

Year: 2001=1,2002=2,2003=3,2004=4...2010=0,2011=1,2012=2..

Month: 1~9, A~C, for Jan. ~ Dec.

Day: 1~9, A~Y, for 1st to 31st, exclude I ,O, and U.

Revision Code: Cover all the change

Serial No.: Manufacturing sequence of product

Product Line: 1 -> Line1, 2 -> Line 2, ...etc.

8. PRECAUTIONS

8.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality. For example, the response time will become slowly.

8.2 STORAGE PRECAUTIONS

- (1) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0°C to 35°C and relative humidity of less than 70%.
- (2) Do not store the TFT – LCD module in direct sunlight.
- (3) The module should be stored in dark place. It is prohibited to apply sunlight or fluorescent light in storing.

8.3 OPERATION PRECAUTIONS

- (1) The LCD product should be operated under normal condition.
Normal condition is defined as below :
Temperature : 20±15°C
Humidity: 65±20%
Display pattern : continually changing pattern(Not stationary)
- (2) If the product will be used in extreme conditions such as high temperature, high humidity, high altitude ,display pattern or operation time etc...It is strongly recommended to contact CMI for application engineering advice . Otherwise , Its reliability and function may not be guaranteed.

8.4 SAFETY PRECAUTIONS

- (1) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (2) After the module's end of life, it is not harmful in case of normal operation and storage.

8.5 SAFETY STANDARDS

The LCD module should be certified with safety regulations as follows:

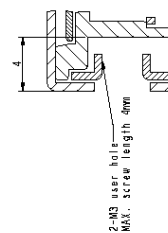
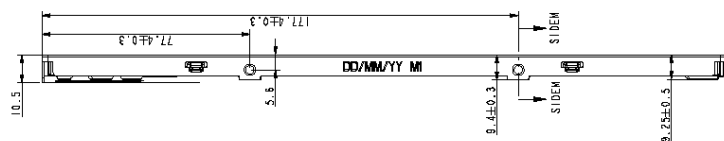
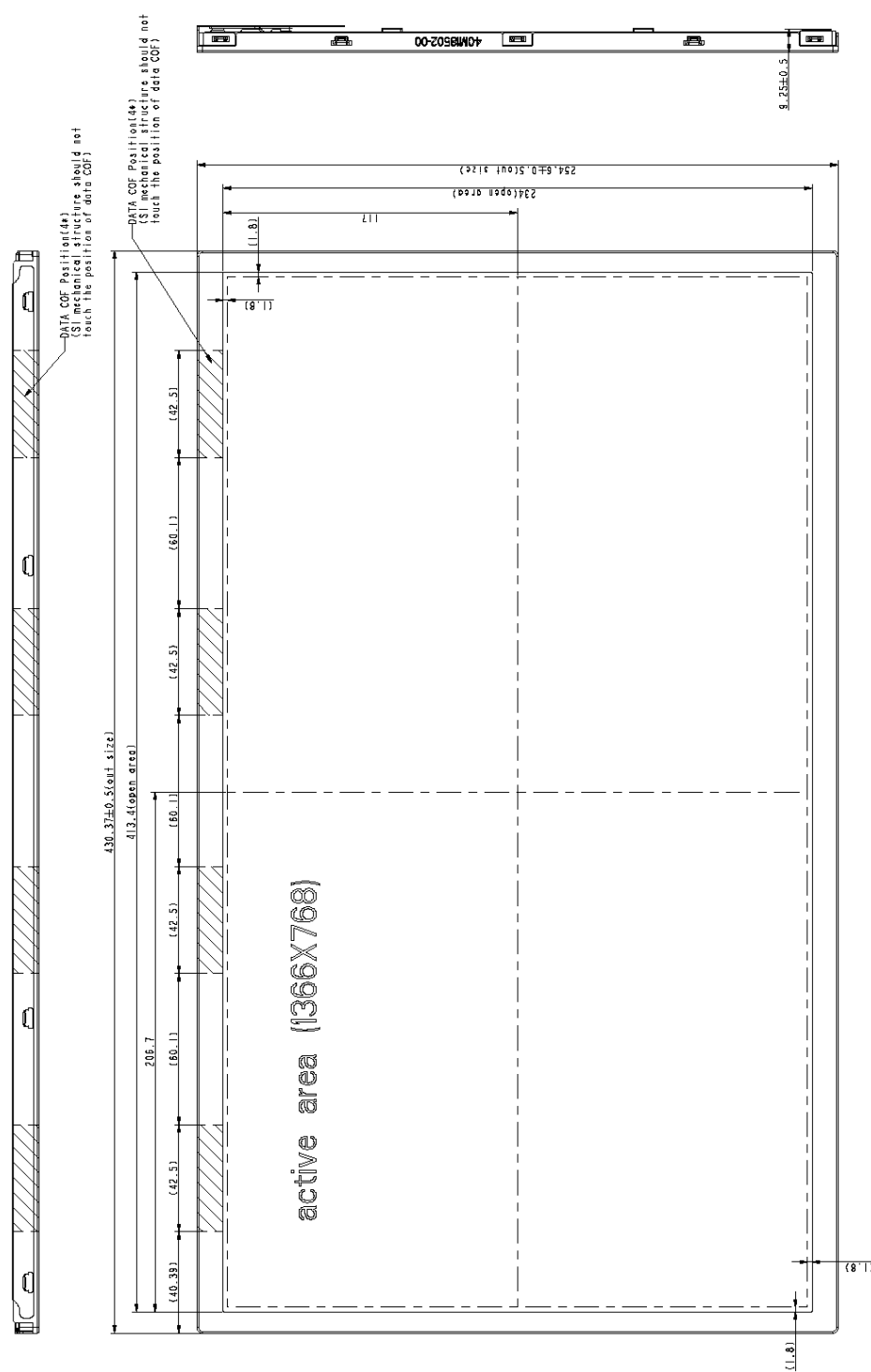
- (1) UL60950-1 or updated standard.
- (2) IEC60950-1 or updated standard.

8.6 OTHER

When fixed patterns are displayed for a long time, remnant image is likely to occur.

Appendix. OUTLINE DRAWING

Refer to next 2 pages.



NOTES:
1.outline tolerance:0.5mm
2.side mount hole rotational torque must be MAX.5kgf-cm

SECTION SIDEM-SIDEM
SCALE 5:1

[illegible]

